

FIG. 1

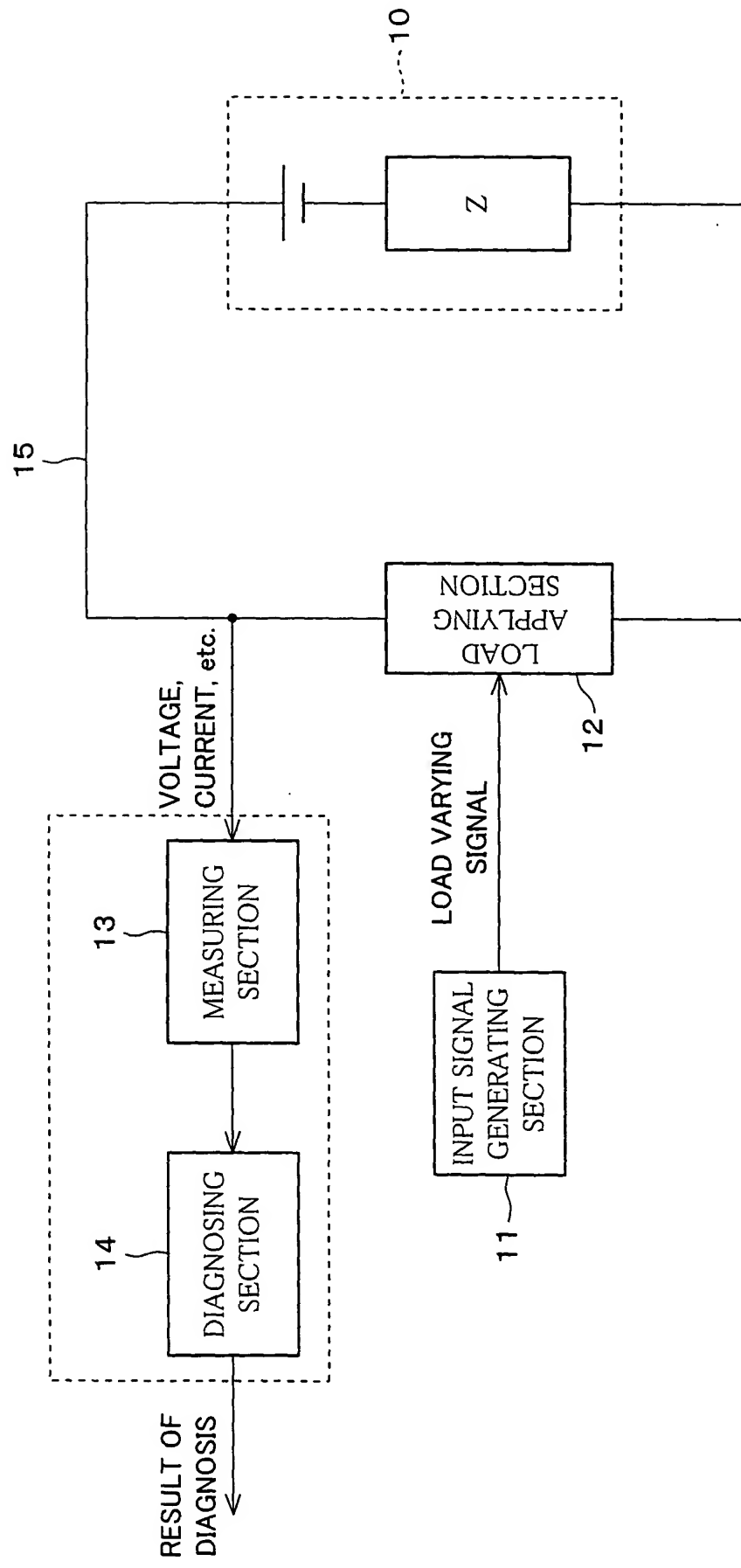


FIG. 2

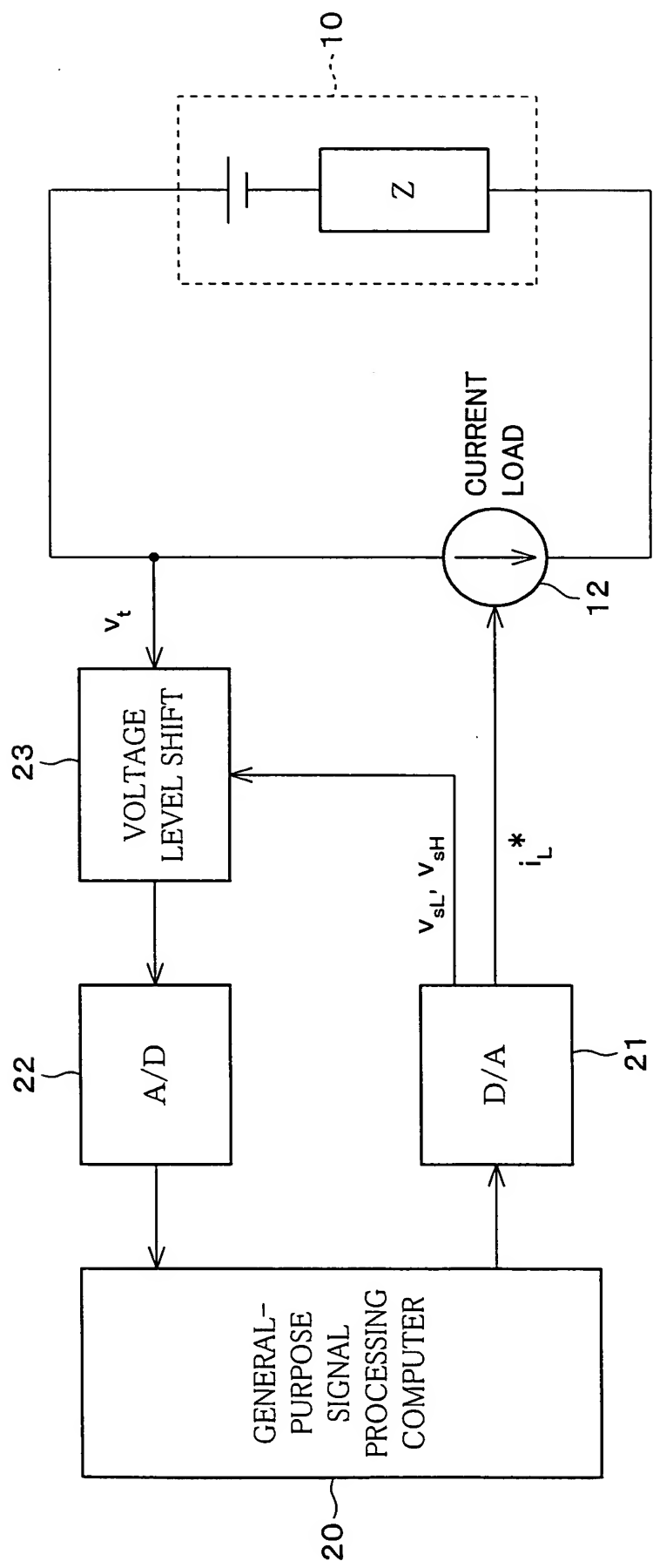


FIG. 3 (a)

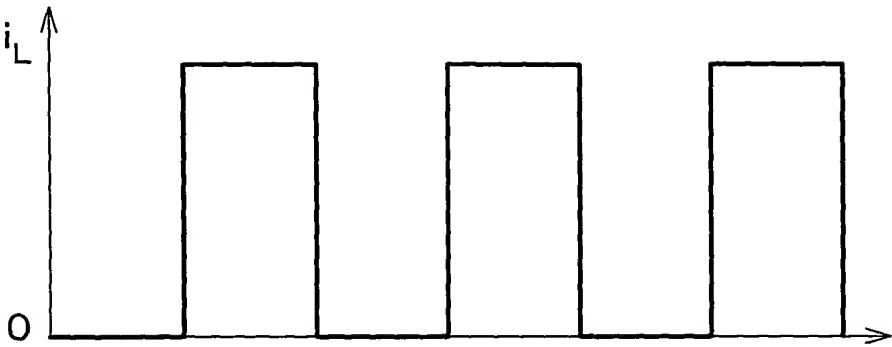


FIG. 3 (b)

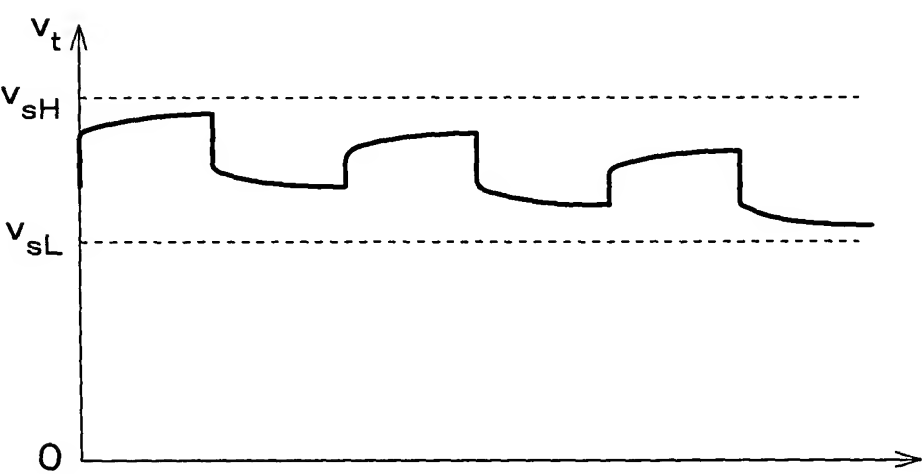


FIG. 4

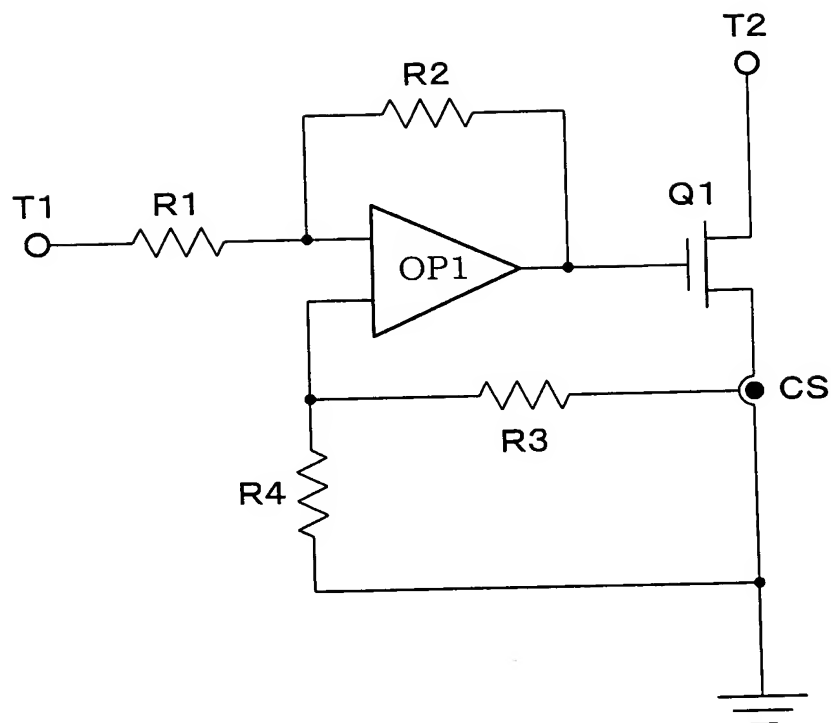


FIG. 5

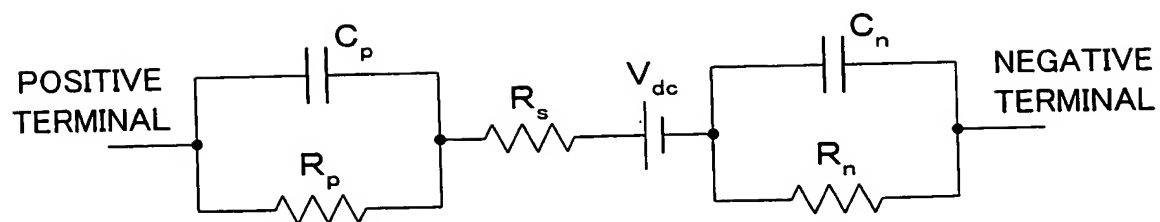


FIG. 6

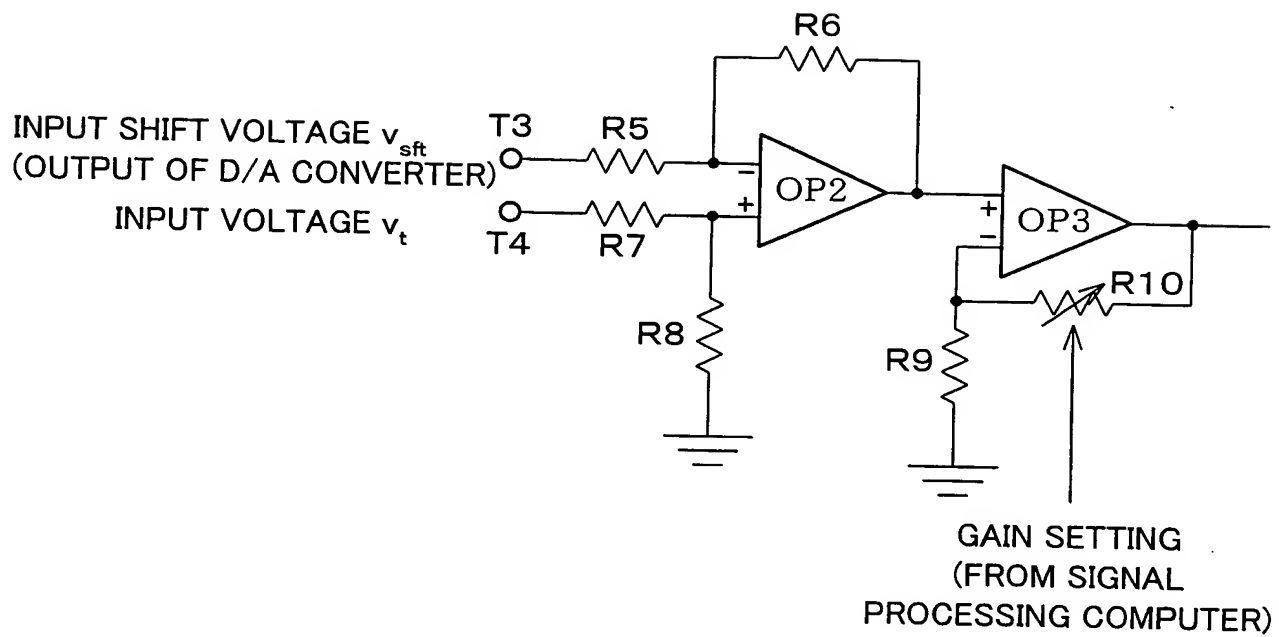


FIG. 7

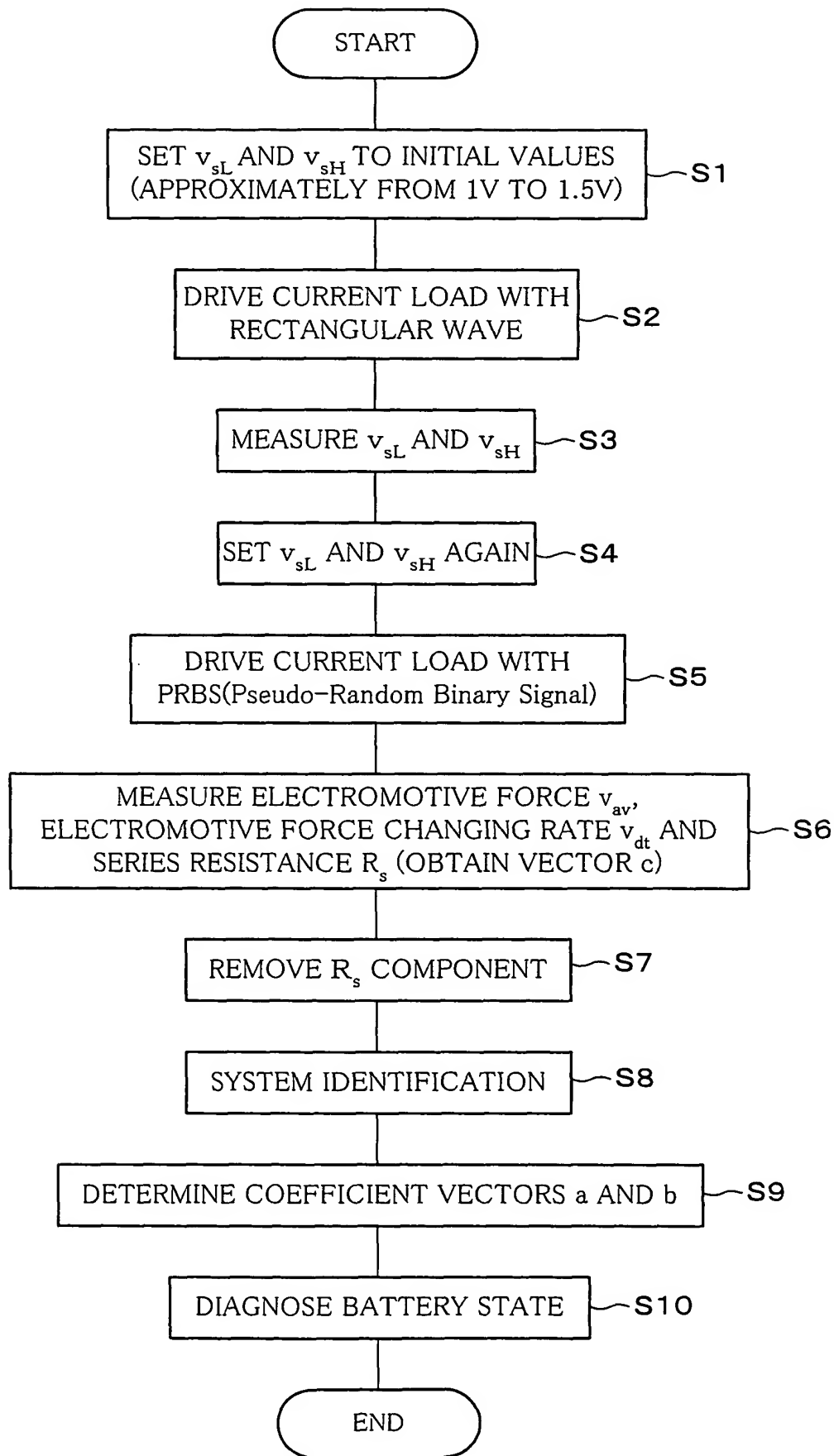


FIG. 8

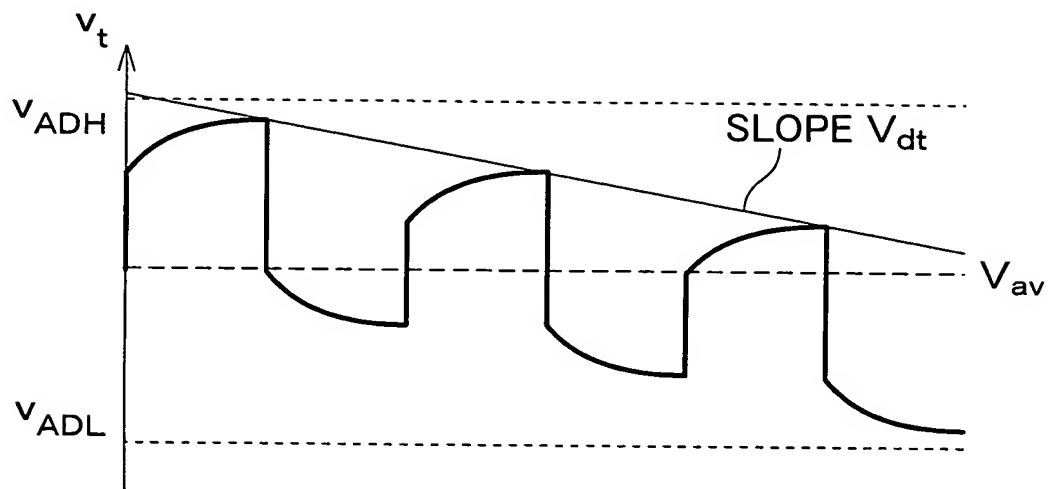


FIG. 9

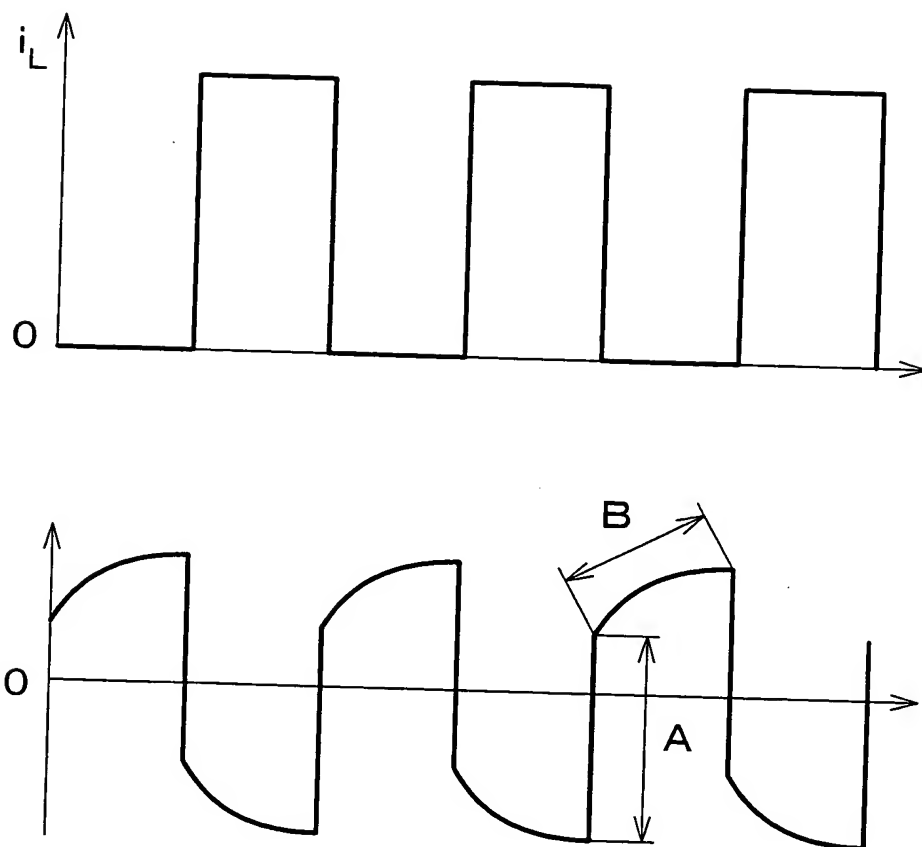
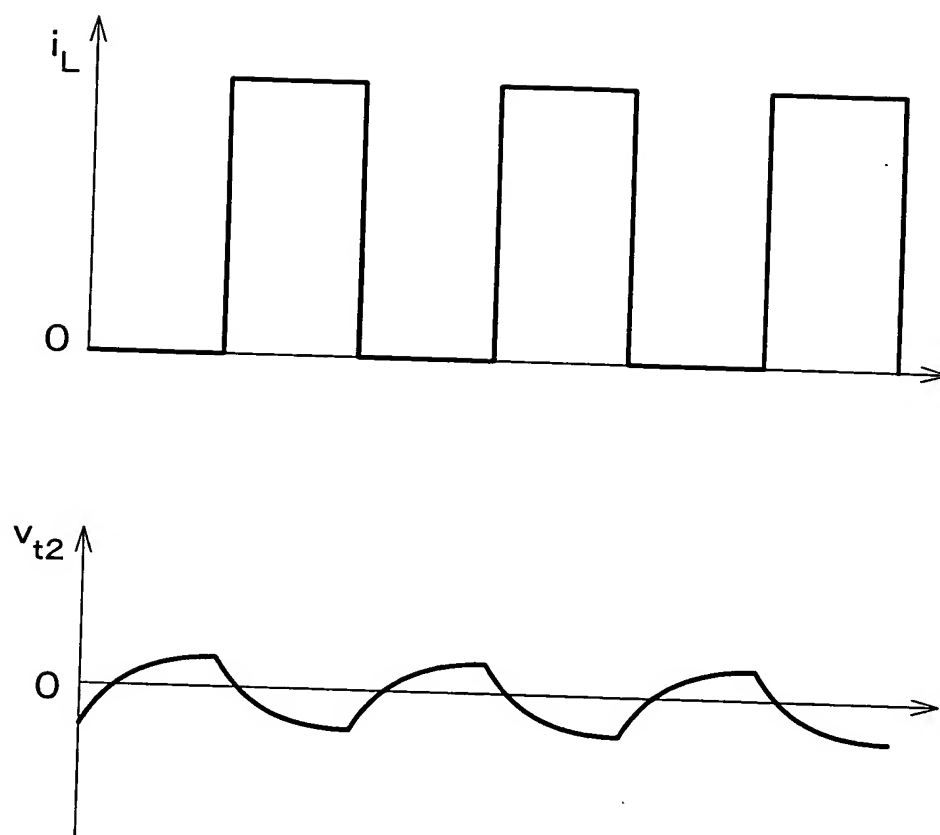


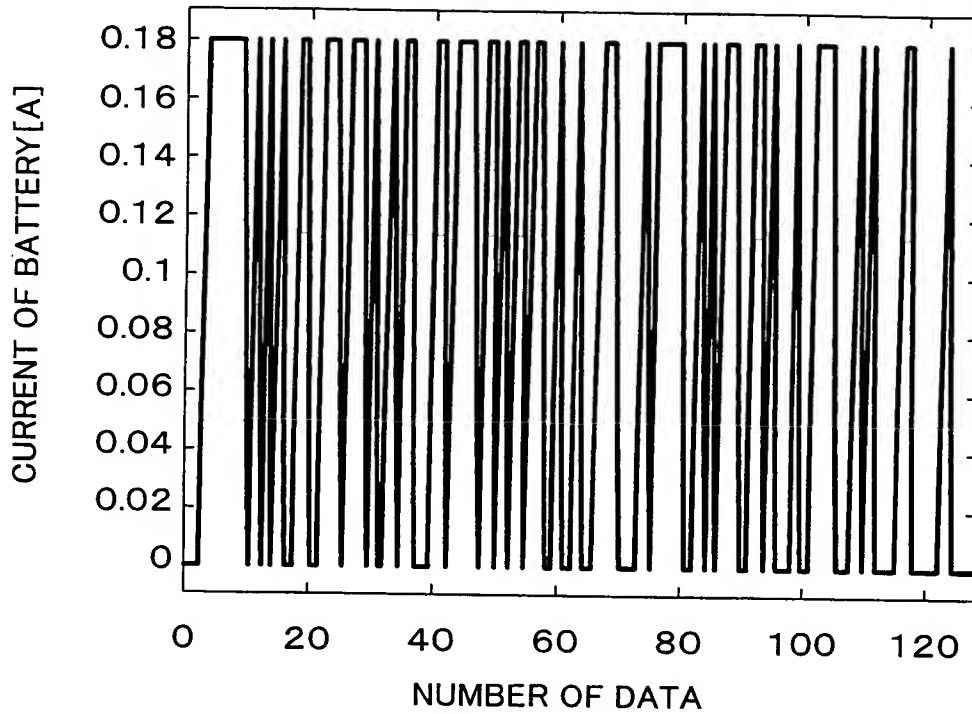


FIG. 10



**FIG. 11 (a)**

INPUT CURRENT SIGNAL



**FIG. 11 (b)**

OUTPUT VOLTAGE SIGNAL

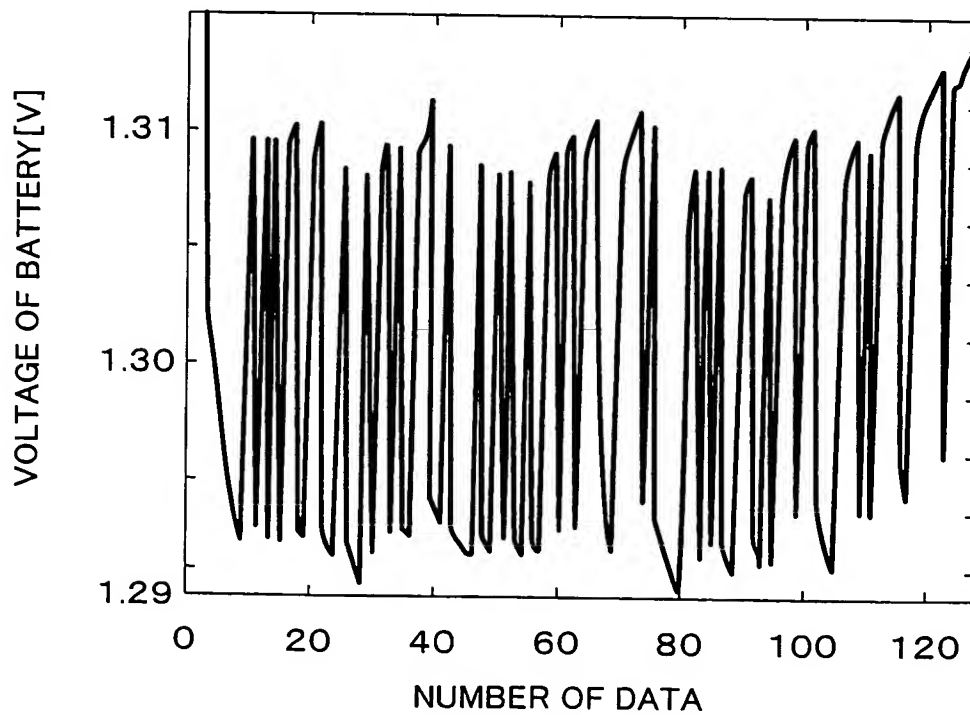


FIG. 12

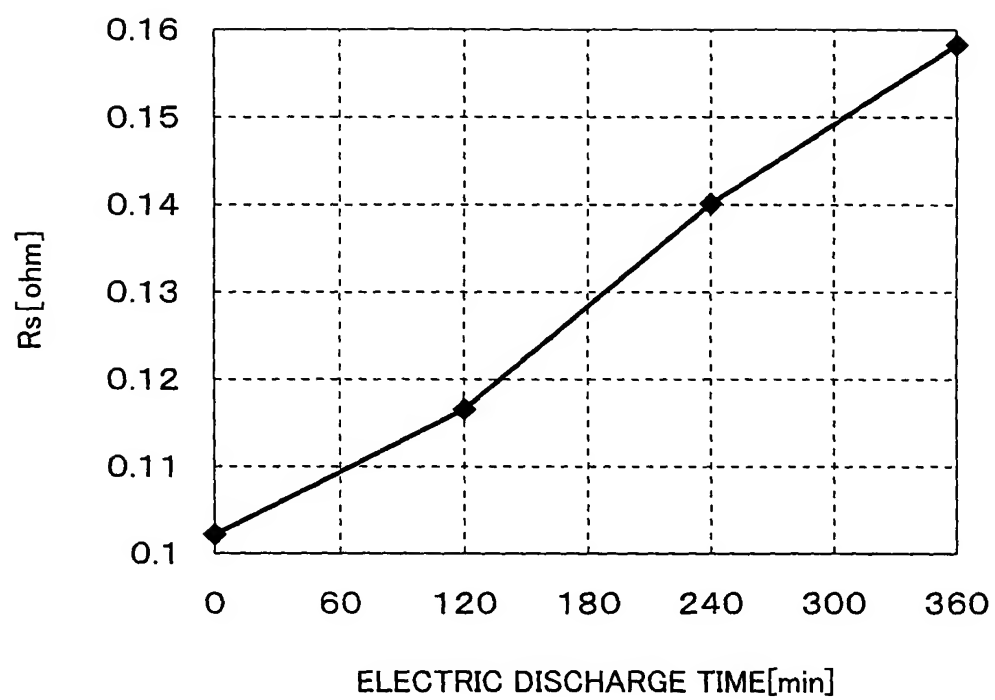


FIG. 13 (a)

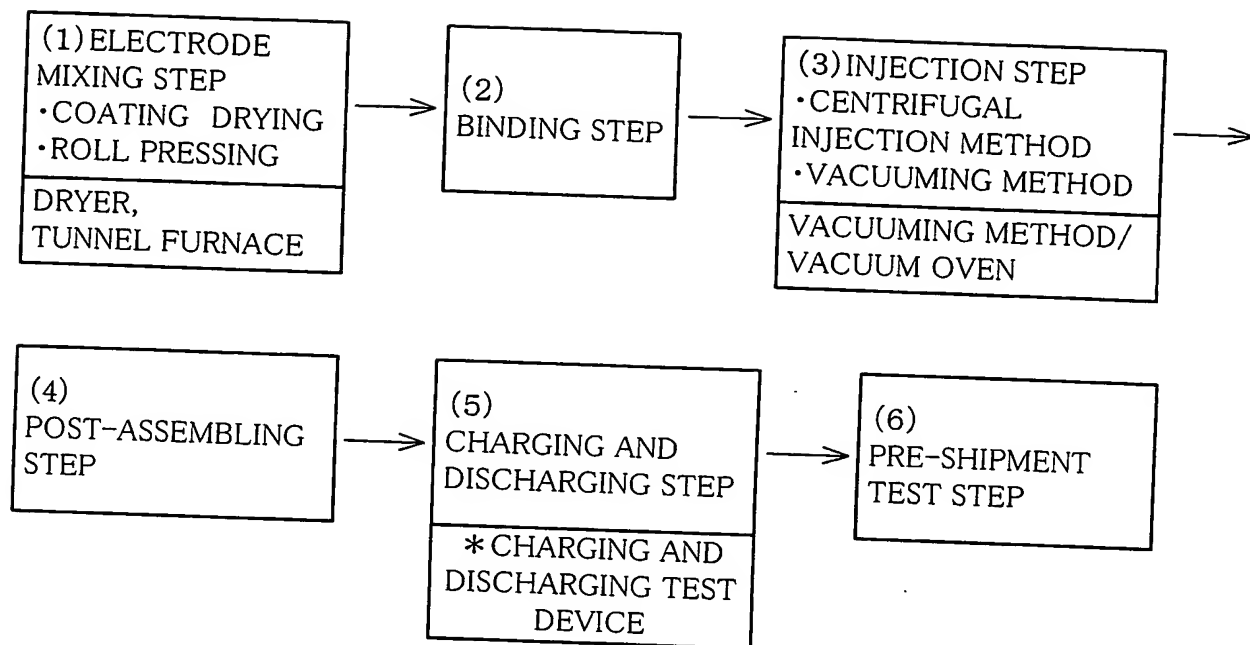


FIG. 13 (b)

